REMARKS

Independent claim 1 calls for generating an interrupt in response to the detection of a frequency change.

Independent claim 1 was rejected as being anticipated by Inoue. Inoue fails to suggest generating an interrupt in response to the detection of a frequency change.

In Inoue, the CPU interrupts primary processing when the measured actual temperature has dropped below a predetermined threshold temperature. *After the interrupt is generated*, the frequency or duty cycle of the clock signal changes in response to the interrupt. See Inoue, col. 10, lines 12-46.
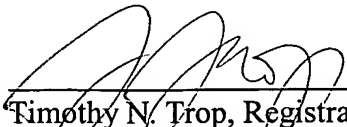
Inoue works in a fashion opposite to what is claimed -- a temperature change is detected, an interrupt is generated and the interrupt causes a frequency change. The frequency change is never detected and therefore detection of a frequency change does not result in generation of an interrupt.

Independent claims 11 and 21 also distinguish over Inoue for reasons similar to those set forth above with respect to independent claim 1.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested.

Respectfully submitted,

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